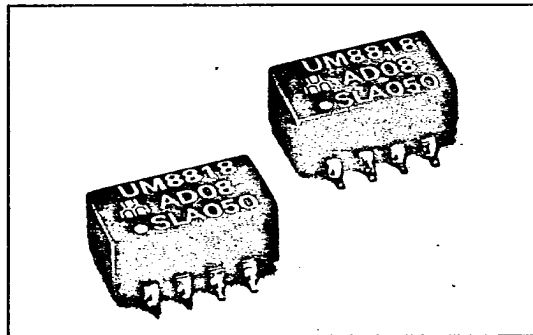




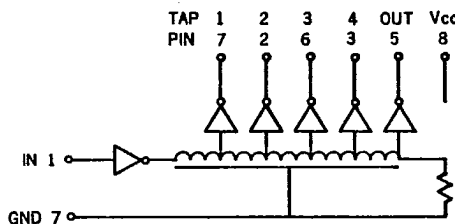
AD08SLA SERIES: 8-PIN 5-TAP SURFACE MOUNTING, L LEAD.



FEATURES:

- Compatible with TTL Circuits
- 5 Equally-Spaced Delay Taps
- Surface Mount L Lead Package
- Operating Temperature Range: 0°C to +70°C
- Custom Designs(Delays or Pin Layouts) Available upon Request

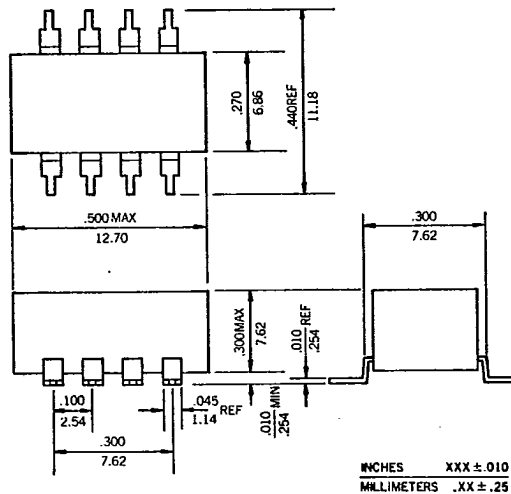
CIRCUIT AND PIN CONNECTIONS:



ELECTRICAL CHARACTERISTICS

Supply Voltage Vcc: 5.0±0.25 VDC
 Logic 1 Input Voltage: 2.0V min.
 Input Current: 50µA max.
 Logic 0 Input Voltage: 0.8V max.
 Input Current: -2.0mA max.
 Logic 1 Output Voltage: 2.4V min.
 Logic 0 Output Voltage: 0.5V max.
 All measurements made at Vcc=5.0V, 25°C

PACKAGE DIMENSIONS:



FAN OUT CAPABILITIES

Logic 0 Output: 10 TTL loads/tap max.
 : 20 TTL loads/unit max.
 Logic 1 Output: 20 TTL loads/unit max.

INPUT TEST CONDITIONS

Pulse Voltage: 3.2 V
 Rise Time: 3.0 ns
 Supply Current: 60 mA typical
 Pulse Width: min. 100% of total delay
 Duty Cycle: 33% or less

ELECTRICAL SPECIFICATIONS:

PART NO.	TOTAL DELAY(1) ns ±5%	TAP DELAY(1) ns	RISE TIME(2) ns max.
AD08SLA025	25 ±2	5 ±2	4
AD08SLA050	50	10 ±2	4
AD08SLA075	75	15 ±2	4
AD08SLA100	100	20 ±3	4
AD08SLA125	125	25 ±3	4
AD08SLA150	150	30 ±3	4
AD08SLA175	175	35 ±3	4
AD08SLA200	200	40 ±3	4
AD08SLA250	250	50 ±3.5	4

(1) Delays. measured at 1.5V level on leading edge only with no loads on taps.
 (2) Rise Time measured from 0.75V to 2.4V with no loads.